

RESUME

Mohammed Arif

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CAREER OBJECTIVE

To work with full dedication for attainment of company objective under a meaningful job content given promising advancement opportunities.

EXPERIENCE

Currently working as an Assistant Professor in Gyan Ganga Institute of Technology & Sciences having experience of 9 year.

EDUCATION QUALIFICATION

Qualification	University	Pass out Year
M TECH	R.G.P.V	Oct2012 with 72% aggregate.
B.E	R.G.P.V	Jun 2008 with 70.03% aggregate.
12 th	M.P.BOARD	2004
10 th	C.B.S.E	2002

PROJECTS

1. Major Project: **Heart Beat Monitor**

It sense the heart beat from the finger and display count in BPM on liquid crystal display by using microcontroller.

2. Minor Project: **Wireless Tachometer**

It detects the speed of rotating motor when kept near it a Wireless device.

THESIS SUBMITTED

Optimization of Microcode BIST by Enhance Fault Coverage for Embedded Memory

THESIS GUIDED

1. Optimization Of Microcode BIST Repair By Enhanced Fault Coverage For Embedded Memory
2. Design Of Low Voltage, Low Power Rail To Rail Operational Transconductance Amplifier With Enhance Gain And Gain Bandwidth Product
3. Analog Phase Lock Loop using CMOS Ring Oscillator in 22 nm Technology
4. Low Power and Area Optimized Using Modified Booth Algorithm Radix-2 And Radix-4
5. FPGA Based Parallel Communication using PCI by developing C code

VOCATIONAL TRAINING

Organization: **Vehicle Factory Jabalpur**

- 1) Duration : 15 days
- 2) Team Size : 5 members
- 3) Over View : Study on Electrical Machines & Telephone Exchange systems.

Organization: **Telecom Factory Jabalpur**

- 1) Duration : 30 days

- 2) Team Size : 10 members
- 3) Over View: Study on RCR (RAX Card Repairing Centre) with microwave communication set up.

COMPUTER SKILLS

Platforms: Windows 7, Window 10, linux
Languages: Basics of C & C++, VerilogHDL, VHDL, Embedded C
Package: M S office, M S Excel, Internet, Microwind, QuartesII, Matlab, labview, Xilinx, etc

HOBBIES

Comparative study of religions, learning new software and tools, reading technical books & visiting new places.

EXTRA CURRICULAR ACTIVITIES

- 1 Trainer in *Training on Hands on Embedded System, Training on Linux and Shell programming, Training on C & C++ etc*
- 2 Conducted FDP On *Hands On Embedded System*
- 3 Attended many Workshop, Seminar, Symposia and Conference like *ICEMT, IOT and M2M Workshop, SKEP Workshop under IEEE, ATVES, CICON, IC²SD*
- 4 Workshop on *Research trends in nanofabrication with TCAD Simulation*, BY PDPM-IITDM, Jabalpur.
- 5 Acquired Successful **Accreditation of NBA** twice in Gyan Ganga Institute of Technology & Sciences while working as a Faculty and Assistant Professor
- 6 Worked as a **Reviewer/Editorial Board** WAEC World Academic E-Conferences <http://www.waeconferences.com/> in Electronics and communication section.
- 7 Worked as a coordinator in the famous event of college JUVENTUS (technical event) and Shown active participation in many events in it.

ACHIEVEMENTS

- 1 Second Prize in International Conference “Optimization of microcode BIST by enhance fault coverage for embedded memory” SCEECs IEEE Conference MANIT BHOPAL
- 2 First Prize in National Conference” optimization of microcode BIST repair by enhanced fault coverage for embedded memory” ICSSD, GGITS Jabalpur
- 3 Participated in Group Discussion Extempore and Paper presentation in colleges
- 2 Won second prize in Science Exhibition in 12th class
- 3 Won many certificates and prizes in dance, recitation, song etc

PAPER PUBLICATION

- 1 “Optimization of microcode BIST by enhance fault coverage for embedded memory” SCEECs IEEE Conference MANIT BHOPAL, Mar 2012
- 2 ” Optimization Of Microcode BIST Repair By Enhanced Fault Coverage For Embedded Memory” ICSSD, GGITS Jabalpur, Feb 2012
- 3 “Design of Low Power and Area optimized ALU using 6 Transistor Full Adder and Pass Transistor Logic based Multiplexer circuits” Int. Journal of Electrical, Electronics & Computing Technology, Vol.8(1), March-May-2013
- 4 “Area Optimized and Low Power using Modified Booth Multiplier for Unsigned

- Numbers”, IJESE Volume-2 Issue-6 ISSN: 2319-6378, April 25, 2014
- 5 “Low Power and Area Optimized Using Modified Booth Algorithm Radix-2 And Radix-4”, IJETAE Volume 4, Issue 4 ISSN 2250-2459, 1Feb2014
 - 6 “FPGA Based Parallel Communication using PCI by developing C code” IJRTS, vol1, issue 9, 8/1/2014
 - 7 “Enhance gain constant Gm low power rail to rail operational transconductance amplifier for wideband application” IJSR, VOL 3, ISSUE 9, 9/1/2014
 - 8 “Low voltage, low power rail to rail operational transconductance amplifier with enhance gain and slew rate” IJERGS, vol 2, issue 5, 9/1/2014
 - 9 "Analog Phase Lock Loop using CMOS Ring Oscillator in 22 nm Technology", International Journal of Advanced Research in Computer Engineering & Technology (IJARCET), 11/1/2014
 - 10 “Design Of Low Voltage, Low Power Rail To Rail Operational Transconductance Amplifier With Enhance Gain And Gain Bandwidth Product” IJSET, vol1, issue 10, 12/1/2014
 - 11 “Design And Development Of Algorithm For AGC”, IJSETR, VOL 3, issue 9, 9/1/2014
 - 12 “Ultra Low Power Design and High Speed Design of Domino Logic Circuit”, IJERA, Vol. 5, Issue 12, (Part - 3), Dec-2015
 - 13 “Comparative Analysis of Domino Logic Circuits for Better Noise and Delay Performance”, (IJETT) – Volume 30 Number 3, Dec-2015

PERSONAL PROFILE

Name : Mohammed Arif
Mother's Name : S. Ansari
Father's Name : A. A. Ansari
Date of Birth : 3 May 1985
Nationality : Indian
Status : Married
Passport No. : H0944227
Languages Known : English, Hindi, and Urdu

DECLARATION:

I hereby declare that all the information given above is true to best of my knowledge.

Place: Jabalpur.

(MOHAMMED ARIF)



Date: 16/08/2017